

Docket No.: TESSERA 3.3-018 CONT CONT II CONT II CON
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Khandros et al.

Application No.: 09/691,807

Group Art Unit: 2815

Filed: October 19, 2000

Examiner: S. Clark

For: STACKED CHIP ASSEMBLY

Commissioner for Patents
Washington, DC 20231

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

It is respectfully requested that the information referred to herein be made of record and considered with respect to the above-referenced U.S. patent application. Information concerning litigation in the International Trade Commission and in a Federal District Court regarding U.S. Patent No. 5,679,977, as well as other U.S. patents, related to the present application is of record in Application No. 09/488,268. The Examiner is referred to copies of the information cited regarding this litigation, which can be found in the file for the '268 application, the benefit of which is claimed under §120. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited information is legally available prior art or that the same are pertinent or material.

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this paper is being facsimile transmitted
to the Patent Office on the date shown below.

Kimberly V. Perry

Signature

March 25, 2002

Date

To: U.S.P.T.O.Attn: S. ClarkFax No.: (703) 308-7722

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In the event that any fee is due in connection with the present Information Disclosure Statement, the Commissioner is hereby authorized to charge the same to our Deposit Account No. 12-1095.

Dated: March 25, 2002

Respectfully submitted,

By 

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